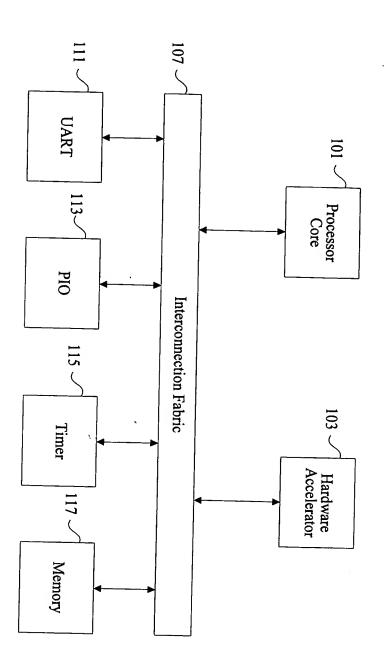
Figure 1



System Board 211 ASSP 203 System On A Programmable Chip 201 Memory 205

Figure 2

Figure 3

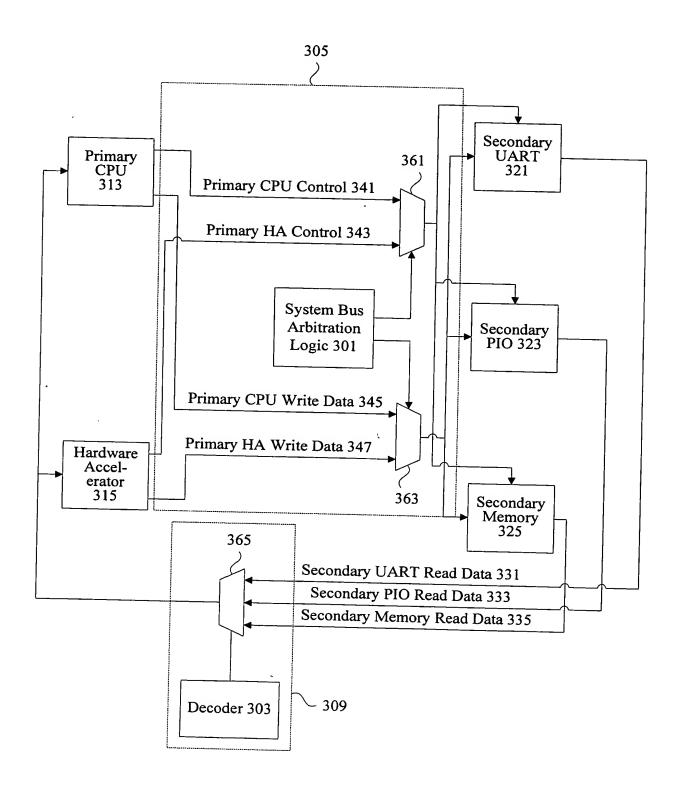
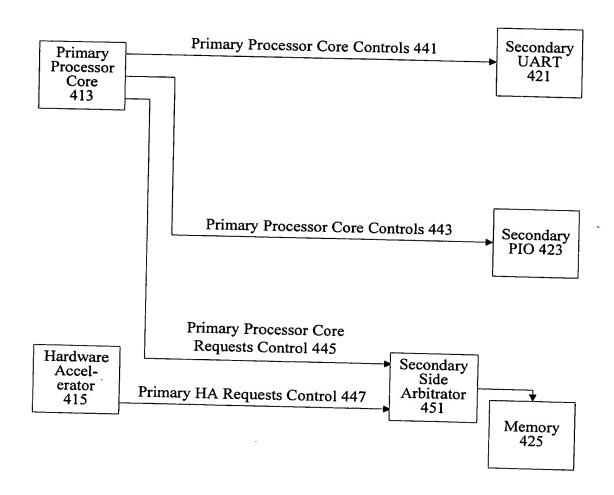


Figure 4



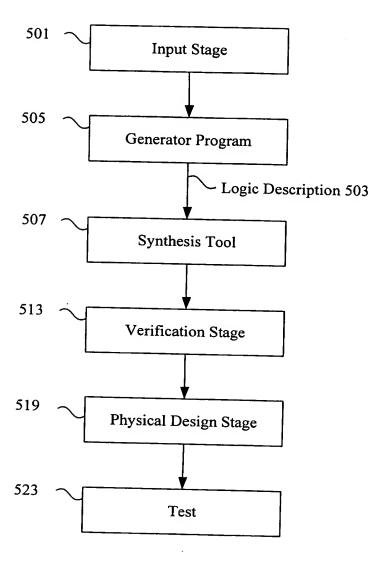
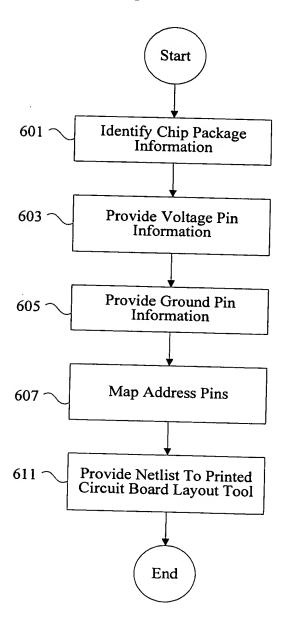


Figure 5

Figure 6



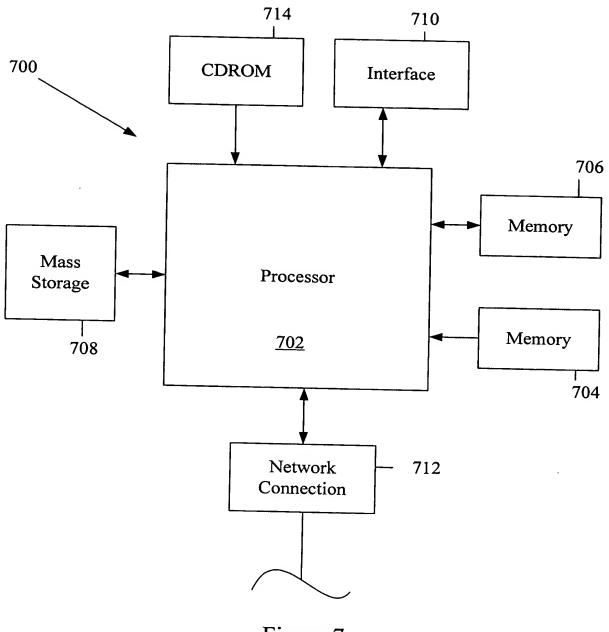


Figure 7